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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/560,109 Confirmation No.: 3400
First Named Inventor: Sallaway, Peter J. Filing Date: 28 April 2000
Group Art Unit: 2734 Examiner: Tran, K.
Atty. Docket No.: M-5628 US
Title: Detector For A Gigabit Ethernet Receiver
Assignee(s): National Semiconductor Corporation

Mountain View, California
15 October 2003

**MAIL STOP NON-FEE AMENDMENT
COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, Virginia 22313-1450**

AMENDMENT TO TEXT

Sir:

Supplemental to the Text Amendment submitted 15 September 2003, please further amend the above patent application in the following manner.

IN THE SPECIFICATION

Page 13, amend the paragraph beginning at line 8 as follows:

A point within subset p is chosen by the six least significant bits of the input, bits 0 through 5. Bits 0 through 5 are input to a 4-D PAM-5 mapper 211 along with the output χ ~~output~~ of set select 210. ~~210, χ~~ 4-D PAM-5 mapper 211 determines the 4-D PAM-5 symbol within set D_p which represents the eight input bits, bits 0 through 7, and the parity bit. Each subset, D_0 ~~D_0~~ through D_7 , contains more than the 64 points required to encode the six bits, bits 0 through 5. These additional points are either used as control characters or not used at all.

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